

APPENDIX B
VERSION WITH MARKINGS TO SHOW CHANGES MADE
37 C.F.R. § 1.121(b)(iii) AND (c)(ii)

CLAIMS:

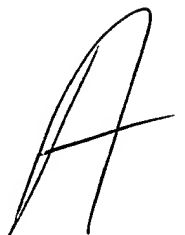
1. An electronic multilayer ceramic component comprising:

a sintered ceramic block having a first end face, a second end face, upper and lower surfaces connecting said first and second end faces to each other, and a pair of side faces;

a plurality of inner electrodes disposed in the sintered ceramic block such that said plurality of inner electrodes overlap, in a thickness direction, with one another via ceramic layers and such that each said inner electrode extends from said first end face toward said second end face but not reaching said second end face or from said second end face toward said first end face but not reaching said first end face; and

a first outer electrode and a second outer electrode formed such that said first and second end faces are covered with said first and second outer electrodes, respectively;

wherein, when the width of the region in which said plurality of inner electrodes are disposed such that said inner electrodes overlap in the thickness direction with one another via ceramic layers is denoted by EW, the width of a side gap region between ends of said inner electrodes and one of side faces of said sintered ceramic block is denoted by WG, the thickness of a region in which said plurality of inner electrodes are disposed such that said inner electrodes overlap in the thickness direction with one another via ceramic layers is denoted by T, and the thickness of one of the outermost ceramic layers parallel to said plurality of inner electrodes and located outside the region in which said plurality of inner electrodes are disposed such that said inner electrodes overlap in the thickness direction with one another via ceramic layers is denoted by G, the following conditions are satisfied:

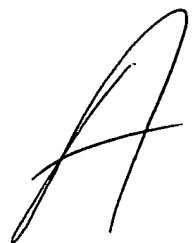


WG (the width of the side gap region between the ends of the inner electrodes and one of the side faces) /EW \geq 0.3,

and

T/G (the thickness of the one outermost ceramic layer) \leq 7.0.

5. An electronic multilayer ceramic component according to Claim 1 or 2, wherein [both] the thickness of each outermost ceramic [layers are denoted by] layer is G.

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